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In re Patent Application of: Sriram R. Vangal et al.
Title: METHOD AND APPARATUS FOR DRIVING DATA PACKETS
Attorney Docket No.: 884.200US1

JCS44 U.S. PTO
09/470080
12/22/99

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UNITED STATES PATENT APPLICATION

METHOD AND APPARATUS FOR DRIVING DATA PACKETS

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SECRET 08002460

METHOD AND APPARATUS FOR DRIVING DATA PACKETS

Field

5 The present invention relates generally to very large scale integration (VLSI) design, and more specifically to driver size and power reduction in shared bus protocol designs.

Background

10

Traditional methods of forwarding data in systems which require data forwarding consist of using a buffer to send a packet of data across a bus interconnect. In order for the design to be usable at each part of the system, for example at each crosspoint in a crossbar network, buffers are sized so as to be able to
15 accommodate the worst case routing and timing situation expected to be encountered. Since not all cases are the worst case, significant amounts of extra buffer space and power consumption are used. Because of the oversizing of the buffers, that is buffers designed to accommodate the worst case scenario, significantly oversized drivers are required. That is, drivers capable of driving data
20 across the largest distance are used even if the distance to be driven is less than the maximum distance.

Larger drivers require more power to operate. The more power required to operate, the greater the power consumption of the system, and the greater the operating temperature of the system. Higher operating temperatures lead to slower
25 operation. Even small amounts of additional power lead to large power waste due to the large number of components present in typical VLSI systems.

Summary

In one embodiment, an apparatus for forwarding data packets includes a controller operatively connected to receive header information from a data packet to
5 be routed through the apparatus, and a legged driver operatively connected to receive leg enable bits from the controller and to receive data packets.

In another embodiment, a method for forwarding data packets includes enabling sufficient legs in a legged driver to power a transfer of a packet from an input location to an output destination.

10 Other embodiments are described and claimed.

Brief Description of the Drawings

Figure 1 is a block diagram of one embodiment of the present invention
15 implemented in a crossbar;

Figure 1A is a block diagram of another embodiment of the present invention;

Figure 2 is a block diagram of one embodiment of driver control circuitry of the present invention;

20 Figure 3 is a circuit diagram of a driver encoder according to an embodiment of the present invention;

Figure 4 is circuit diagram of a driver according to an embodiment of the present invention;

25 Figure 5 is a flow chart diagram of a method embodiment of the present invention; and

Figure 6 is a flow chart diagram of another method embodiment of the present invention.

Description of Embodiments

In the following detailed description of embodiments, reference is made to the accompanying drawings which form a part hereof, and in which are shown by way of illustration specific embodiments in which the invention may be practiced. These embodiments are described in sufficient detail to enable those skilled in the art to practice the invention, and it is to be understood that other embodiments may be utilized and logical, structural, electrical, and other changes may be made without departing from the scope of the present invention.

Figure 1 illustrates a packet forwarding apparatus 100 according to one embodiment of the present invention. Packet forwarding apparatus 100 is shown as implemented in a generic crossbar, although the invention is not so limited. Any shared bus protocol in which the destination of a packet is known may employ the concepts of the present invention without departing from its scope. Further, any VLSI design which uses a floor plan with different driver size requirements may employ embodiments of the present invention without departing from its scope, provided the destination of the packet is known. Shared busses are common in all manner of integrated circuits, and the concepts of the present invention are applicable in all forms of shared bus situations as well.

In the packet forwarding apparatus 100, a plurality of input queues 102, 104, 106, and 108 are each connected to a legged driver 110, 112, 114, and 116 respectively. The drivers 110, 112, 114, and 116 are each operatively connected to a shared data bus 118. The data bus 118 is operatively connected to a plurality of output destinations 120, 122, 124, and 126.

A data packet presented at any one of the input queues may be destined to any one of the output destinations. As has been mentioned, in typical crossbar configurations, the driver is sized and powered to accommodate the longest distance any data packet may be routed from any queue to any output destination. As may be seen, only two distances in the actual configuration will require the largest driver size and power, namely input queue 102 to output destination 126, and input queue 108 to

output destination 120. The remaining distances are less than the largest distance required, and hence do not require the full power of the driver. The driver is shown in greater detail in Figure 4.

5 In one embodiment of the present invention, the data packets presented to input queues 102, 104, 106, and 108 each have an added bit or series of bits, referred to as a destination identification (DID) that indicate the destination of the packet. In one embodiment, each of the drivers is assigned a unique location identification (LID) to specify its spatial location in the array. Each LID is in one embodiment hard-wired into the driver. The strength of the driver used for powering the transfer
10 of data packets to their assigned destinations is determined using the DID for each specific packet and the LID. In one embodiment, the distance of travel for the data packet is determined by a logical subtraction of the DID of the data packet and the LID of the driver driving the data packet to its destination. Other determination schemes will be evident to those of skill in the art, and are within the scope of the
15 invention. The result of the subtraction indicates the distance from the driver to the packet destination. This result in one embodiment is encoded and buffered to control the output driver. The output driver is in one embodiment a legged driver which enables or disables further driver strength depending upon the determined distance the current packet is to travel to its destination.

20 In one embodiment, the encoding scheme is selected so that when the packet location to destination difference is zero, that is when the DID and the LID are for the same port, then only one leg of the driver is turned on. If the packet location to destination distance is one port, for example, driver input queue 102 to output destination 120, only one leg of the driver 110, 112, 114, or 116 is enabled. If the
25 DID and the LID are for ports immediately adjacent one another, then two legs of the driver are enabled. At the maximum routable distance between the DID and the LID, all legs of the driver are enabled. In all instances where the DID and the LID are not separated by the maximum distance, the apparatus 100 and drivers 110, 112, 114, and 116 consume less power than traditional drivers.

While four input queues and four output destinations are shown in the apparatus 100, it should be understood that the embodiments of the present invention are scalable to any number of input queues and output destinations without departing from the scope of the invention.

5 Figure 1A is a block diagram of an apparatus embodiment 150 for forwarding data packets. Apparatus 150 comprises a controller 152 operatively connected to receive header information from a data packet to be routed through the apparatus 150, and a legged driver 154 operatively connected to receive leg enable bits from the controller 152 and to receive data packets. Controller 152 comprises in one
10 embodiment a subtractor 156 and an encoder 158. The subtractor 156 has as inputs in this embodiment data packet header information bits (DID) indicating the destination of the data packet, and hard-wired location identification bits (LID) indicating the spatial location of the apparatus 150 in the system.

 The subtractor 156 logically subtracts the DID and the LID to generate
15 signals indicative of the distance between the apparatus 150 and the destination of the data packet. The subtractor output is presented to encoder 158 in one embodiment. Encoder 158 translates the subtractor output to driver leg enable signals which enable or disable legs of driver 154 depending upon the determined distance between the apparatus and the data packet destination. In another
20 embodiment, the output of the subtractor 156 is presented directly to the driver 154 to control the enablement of legs of the driver 154.

 The various components of the apparatuses 100 and 150 are shown in greater detail below. Figure 2 is a block diagram of a legged driver control circuit 200 according to one embodiment of the invention. Legged driver control circuit 200
25 comprises subtractor 202, encoder 204, and driver 206. In one embodiment, hard-wired bits are used to provide information about the spatial location of the driver to which the data packet is presented in the floorplan of the VLSI circuit, in the embodiment shown a crossbar network. In another embodiment, a scan is used to provide the information. It should be understood that any means for providing a
30 unique identifier for a driver is acceptable, and is within the scope of the invention.

It should be understood that a different number of input queue and driver locations, and therefore driver legs may be used in various embodiments of the invention. With four driver legs, two hard-wired bits for the LID and two destination bits for the DID are used. For a configuration with greater than four and up to eight input queues, drivers, and destinations, three LID and DID bits are used. It should be seen that the embodiments are scalable to any size driver, input, and destination configuration.

The output bits of the subtractor 202, in this embodiment two subtract bits, s0 and s1, are presented to encoder 204 for encoding to the driver 206, the number of driver legs that should be enabled to sufficiently power the driver to route the packet to its destination. The encoder 204 in this embodiment generates three driver enable bits which, along with their complements, are presented to the legged driver to enable sufficient driver legs to supply enough driver strength to route the packet to its destination. No extra driver power is enabled, so the most efficient use of power resources is made in the embodiments of the invention. The subtract bits are an indication of the distance between the packet destination and the input queue.

Figure 3 shows an embodiment of the legged driver encoder 204. Encoder 204 comprises a series of logic components configured to generate driver leg enable bits en1, en2, and en3, and their complements enn1, enn2, and enn3. Subtract bits s0 and s1 are presented to encoder 204. Subtract bit s0 is presented to one of the inputs of NOR gate 302 and one of the inputs of NAND gate 304. Subtract bit s1 is presented to the other input of NOR gate 302, to the other input of NAND gate 304, and to inverter 306. The resulting outputs of NOR gate 302, NAND gate 304, and inverter 306 are inverted to generate the enable bits en1, en2, and en3. The outputs of NOR gate 302, NAND gate 304, and inverter 306 comprise the enable complement bits enn1, enn2, and enn3.

5 The encoder 204 is used in this embodiment as a two to four encoder. That is, for two subtract bits, four destinations can be generated. For a configuration with three subtract bits, the encoder is a three to eight encoder. In one embodiment, one least significant bit remains on at all times in the encoder scheme. When one least significant bit is on at all times, the driver will always have at least its minimum power. This prevents the driver from floating, which would result in a floating bus in which the state of the signal the bus is in is unknown. The encoder 204 is used in this embodiment to allow increased flexibility for the result of the subtraction of the DID and LID.

10 In another embodiment, when less flexibility is desired or acceptable, the result of the subtraction, that is the subtract bits, may be used to directly control the enablement of the legs of the driver. This would in the case of a two bit subtractor result in a two leg driver, which still provides significant power savings in VLSI designs.

15 Figure 4 shows a driver 400 according to one embodiment of the present invention. Driver 400 comprises four legs 402, 404, 406, and 408. The driver 400 is arranged in cascode fashion. Driver 400 has in this embodiment four strengths, which is determined by which legs are enabled by the generated encoder signals en1, en2, en3 and their complements enn1, enn2, and enn3, and signals en0 and enn0, which are tied to logic high and logic low respectively. The driver 400 in one embodiment comprises four legs 402, 404, 406, and 408, each of which is connected to an enable bit and its complement. The enable bits determine which legs of the driver 400 are enabled at any given time. In the embodiment shown, leg 402 of driver 400 is enabled for a DID and LID which are of the same port as described above. Legs 402 and 404 are enabled if the DID and LID are in immediately adjacent ports. When the DID and the LID indicate a maximum distance between the driver and the destination, all legs 402, 404, 406, and 408 of driver 400 are enabled.

20 In one embodiment, each leg of driver 400 comprises a NAND gate and a NOR gate having inputs connected as shown to an enable bit and its complement

from the encoder, and to the data packet, and outputs connected to the gates of transistors in an inverter as shown in Figure 4. Leg 402 is connected to the en0 and enn0 signals, leg 404 is connected to the en1 and enn1 signals, leg 406 is connected to the en2 and enn2 signals, and leg 408 is connected to the en3 and enn3 signals. As the distance between the driver and the destination of the data packet increases, more legs of the driver are enabled by the encoder signals, which translate the logical subtraction result to an indication of the distance between the driver and the destination of the data in the packet

In one embodiment, the driver legs are of equal strength, that is, the driver legs are linearly related. Each additional leg of the driver adds as much power as the next leg. In another embodiment, the legs of the driver are of exponentially increasing strength. For example, the second leg may have twice the strength of the first, and the third four times the first, and so on. It should be understood that the relative strengths of the driver legs may be varied without departing from the scope of the invention.

In operation, the embodiments shown function as follows. Each data packet presented for routing in the system 100 contains a header which includes destination identification bits (DID) that indicate the destination of the data packet in the system, as well as the main data to be routed to its destination. The packet or destination identification bits DID are shown as d0 and d1 in Figure 2. It should be understood that additional destination identification bits are used when additional input queues, drivers, and destinations are used. Each input port or queue 102, 104, 106, and 108 has a spatial location in the apparatus 100. Each input port or queue is uniquely identified with a location identification LID by hard-wired bits shown as id0 and id1 in Figure 2. The LID and DID are combined in a subtractor 202 to generate subtract bits s0 and s1 which are indicative of the distance between the location of the packet and its destination. The result of the subtraction of subtractor 202 is encoded by encoder 204 to enable a specific number of legs of driver 206. The farther the distance between the driver location and the destination location, the greater the number of legs of driver 206 enabled.

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A method 500 for driver selection is shown in Figure 5 to comprise determining a current location identification in block 502, determining a destination location identification in block 504, determining a difference indicative of a distance between the current location and the destination identification in block 506, and
5 enabling driver strength according to the determined difference in block 508. The current location identification (LID) is in one embodiment hard wired to the driver, and each packet has identified with it destination identification bits (DID) as the packet header. The DID and LID bits are logically subtracted to obtain a subtractor output which is indicative of the distance between the driver and the destination
10 location. The strength of the driver is variable and depends on the determined difference between the current location and the destination location.

The difference between the DID and LID bits is determined in one embodiment by logical subtraction of the bits. The determined difference is an indication of the distance between the driver and the destination. The subtraction
15 result bits are encoded to enable or disable legs of the driver corresponding to the distance the packet must be routed. For example, in one embodiment, the first leg of a driver is always on. As the distance between driver and destination increases, the subtraction result of the logical subtraction of DID and LID increases, and more legs of the driver are enabled.

20 An embodiment of a method 600 for configuring driver size in a legged driver system is shown in Figure 6 to comprise determining a spatial location of a driver in block 602, determining a destination location of a packet at the driver in block 604, determining a distance between the spatial location and the destination location in block 606, and setting driver strength according to the determined
25 distance in block 608. The determination of the spatial location of a driver is in one embodiment accomplished through hard-wiring the location of the driver. The destination location of a packet presented to the driver is determined in one embodiment by destination identification bits added to the packet header. The destination identification bits identify the final destination of the packet of data.
30 Since the current location and the destination location are known, an indication of the

distance between the two locations is obtained in one embodiment by a logical subtraction of the destination location and the current location. Once the subtraction identifies the distance between the destination location and the current location, driver strength is adjusted to enable only those legs of a legged driver necessary to
5 provide enough power to route the data packet to its destination.

The apparatus and methods of the present invention in its various embodiments as described above reduce power consumption from a standard driver configuration. The power savings comes from the conditional enablement of driver legs of the output drivers such as drivers 110, 112, 114, 116, and 206. The power
10 consumed by the drivers described above will be equal to the conventional driver power consumption only if all data packets travel from the farthest points of the array at all times. This is an extremely unlikely traffic pattern.

Further, the driver embodiments of the present invention as described above operate at lower average temperatures, and result in a cooler part due to a reduction
15 in average peak current. Cooler parts operate faster than hotter parts, so the embodiments of the invention run faster than conventional drivers due to the reduced operating temperature. Less current is required for operation of the legged driver when fewer than all of the legs are enabled.

Still further, the embodiments of the present invention reduce cross talk
20 between elements because the peak currents are reduced. Because of the reduction in cross talk due to a reduction in peak current, elements may be laid out closer together in the array, resulting in higher design density.

Although specific embodiments have been illustrated and described herein, it will be appreciated by those of ordinary skill in the art that any arrangement which is
25 calculated to achieve the same purpose may be substituted for the specific embodiments shown. This application is intended to cover any adaptations or variations of the invention. It is intended that this invention be limited only by the following claims, and the full scope of equivalents thereof.

What is claimed is:

- 1 1. A method for driver selection, comprising:
2 determining a current location identification;
3 determining a destination location identification;
4 determining a distance between the current location and the destination
5 location;
6 enabling a driver strength according to the determined distance.
- 1 2. The method of claim 1, wherein determining a current location identification
2 comprises interpreting hard-wired identification location bits of the driver.
- 1 3. The method of claim 1, wherein determining a destination location
2 identification comprises reading a plurality of destination location bits appended to a
3 data packet.
- 1 4. The method of claim 1, wherein determining the distance between the current
2 location and the destination locations comprises determining a logical subtraction of
3 the destination location bits and the hard-wired identification location bits.
- 1 5. The method of claim 4, wherein determining the distance further comprises
2 encoding the logical subtraction result bits in an encoder.
- 1 6. The method of claim 1, wherein enabling the driver strength comprises
2 enabling legs of the driver according to the determined distance.

1 7. A method for configuring driver size in a legged driver system, comprising:
 2 determining a spatial location of a driver;
 3 determining a destination location of a packet at the driver;
 4 determining a distance between the spatial location and the destination
 5 location;
 6 setting driver strength according to the determined distance.

1 8. The method of claim 7, wherein determining a spatial location of a driver
 2 comprises interpreting hard-wired location information for the driver.

1 9. The method of claim 7, wherein determining a destination location of a
 2 packet at the driver comprises interpreting destination identification bits in the data
 3 packet.

1 10. The method of claim 7, wherein determining a distance comprises logically
 2 subtracting the destination location from the spatial location.

1 11. The method of claim 7, wherein setting driver strength comprises enabling
 2 legs of the driver sufficient to power transfer over a data bus of the packet from the
 3 spatial location of the driver to the destination location.

1 12. A method for forwarding packets in a legged driver, comprising:
 2 enabling sufficient legs in the legged driver to power a transfer of a packet
 3 from an input location to an output destination.

1 13. The method of claim 12, wherein enabling sufficient legs comprises:
 2 determining a spatial location of the legged driver;
 3 determining the output destination;
 4 determining a distance from the legged driver to the output destination; and
 5 enabling the legs based on the determined distance.

1 14. Apparatus for forwarding data packets, comprising:
 2 a controller operatively connected to receive header information from a data
 3 packet, the controller to generate leg enable bits; and
 4 a driver having a plurality of legs, the driver operatively connected to receive
 5 leg enable bits from the controller and to receive data packets.

1 15. The apparatus of claim 14, wherein each of the legs of the driver is identical.

1 16. The apparatus of claim 14, wherein each of the legs of the driver has a
 2 different power.

1 17. The apparatus of claim 16, wherein each subsequent leg of the driver is twice
 2 as powerful as the previous leg.

1 18. The apparatus of claim 14, wherein the controller comprises:
 2 a subtractor having a plurality of inputs connectable to receive data packet
 3 header information bits (DID) and spatial location identification bits
 4 (LID), the subtractor to generate subtract bits indicative of the
 5 distance between the apparatus and a destination location for the data
 6 packet; and
 7 an encoder to receive the subtract bits and to encode a plurality of enable bits
 8 to enable legs of the driver according to the distance between the
 9 apparatus and the destination location.

1 19. The apparatus of claim 18, wherein the encoder comprises:
 2 a NOR gate having two inputs connectable to a pair of external signals
 3 representative of a difference in a driver location and a destination
 4 location, and an output;

5 a NAND gate having two inputs connectable to the pair of external signals,
6 and an output; and
7 an inverter connectable to one of the pair of external signals, the outputs of
8 the NOR gate, NAND gate, and inverter representative of encoder bits
9 indicating a number of driver legs to be enabled.

1 20. Apparatus for efficient forwarding of data packets, comprising:
2 a driver having a plurality of legs selectively enableable to provide different
3 driver powers; and
4 a logical subtractor to receive data packet destination information and driver
5 location information and to generate enable signals representative of a
6 distance between the driver location and the destination location to
7 selectively enable legs of the legged driver.

1 21. The apparatus of claim 20, and further comprising:
2 an encoder operatively connected between the driver and the subtractor to
3 receive the enable signals and to encode the enable signals to control
4 the driver strength.

1 22. The apparatus of claim 21, wherein the encoder comprises:
2 a NOR gate having two inputs connectable to a pair of external signals
3 representative of a difference in a driver location and a destination
4 location, and an output;
5 a NAND gate having two inputs connectable to the pair of external signals,
6 and an output; and
7 an inverter connectable to one of the pair of external signals, the outputs of
8 the NOR gate, NAND gate, and inverter representative of encoder bits
9 indicating a number of driver legs to be enabled.

- 1 23. A legged driver, comprising:
2 a plurality of driver legs, the driver legs sequentially enableable by a set of
3 external enable signals to provide multiple driver strengths.
- 1 24. The driver of claim 23, wherein each of the plurality of legs is identical.
- 1 25. The driver of claim 23, wherein each of the plurality of legs has a different
2 strength.
- 1 26. A driver encoder, comprising:
2 a NOR gate having two inputs connectable to a pair of external signals
3 representative of a difference in a driver location and a destination
4 location, and an output;
5 a NAND gate having two inputs connectable to the pair of external signals,
6 and an output; and
7 an inverter connectable to one of the pair of external signals, the outputs of
8 the NOR gate, NAND gate, and inverter representative of encoder bits
9 indicating a number of driver legs to be enabled.
- 1 27. Apparatus for forwarding data packets, comprising:
2 a driver having a plurality of legs, the driver to provide different power levels
3 to forward a data packet, the power levels dependent upon a distance
4 between the driver and a destination location of the data packet; and
5 means for determining the distance between the driver and the destination
6 location and to set a driver power level sufficient to forward the data
7 packet.

Abstract of the Disclosure

An apparatus for driver power and size selection includes in one embodiment a controller for controlling the enabling and disabling of legs in a legged driver, the legged driver providing only that amount of power necessary to transfer a data packet from its current location to its destination location. A method of forwarding data packets includes determining the distance between a current location of a data packet and the destination location of the data packet, and enabling as many legs of a legged driver as are necessary to power the transfer of the data packet to its destination.

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09470080-12399
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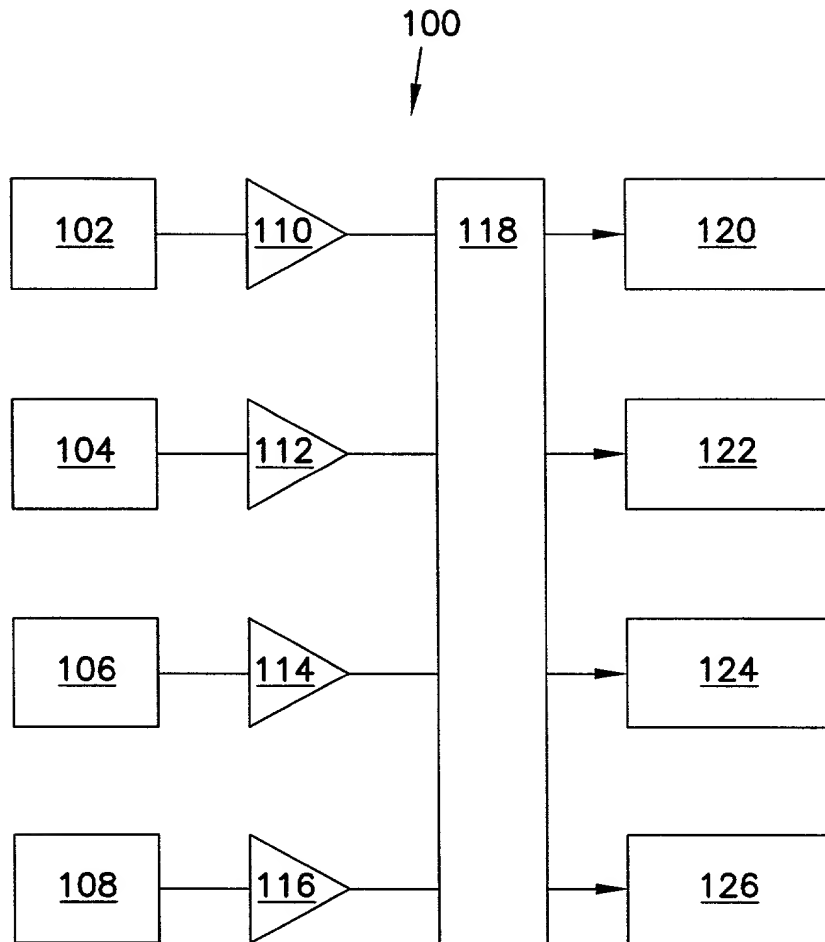


FIG. 1

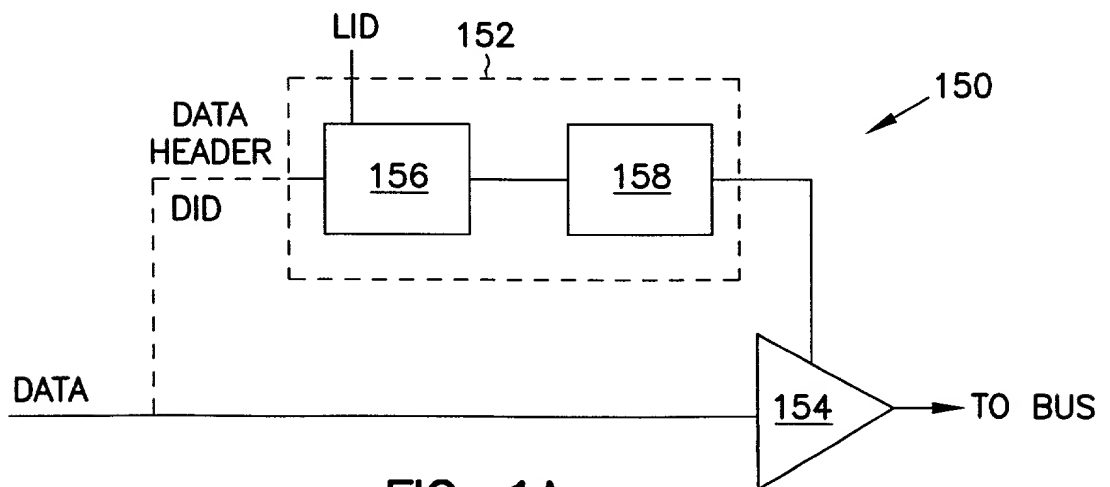


FIG. 1A

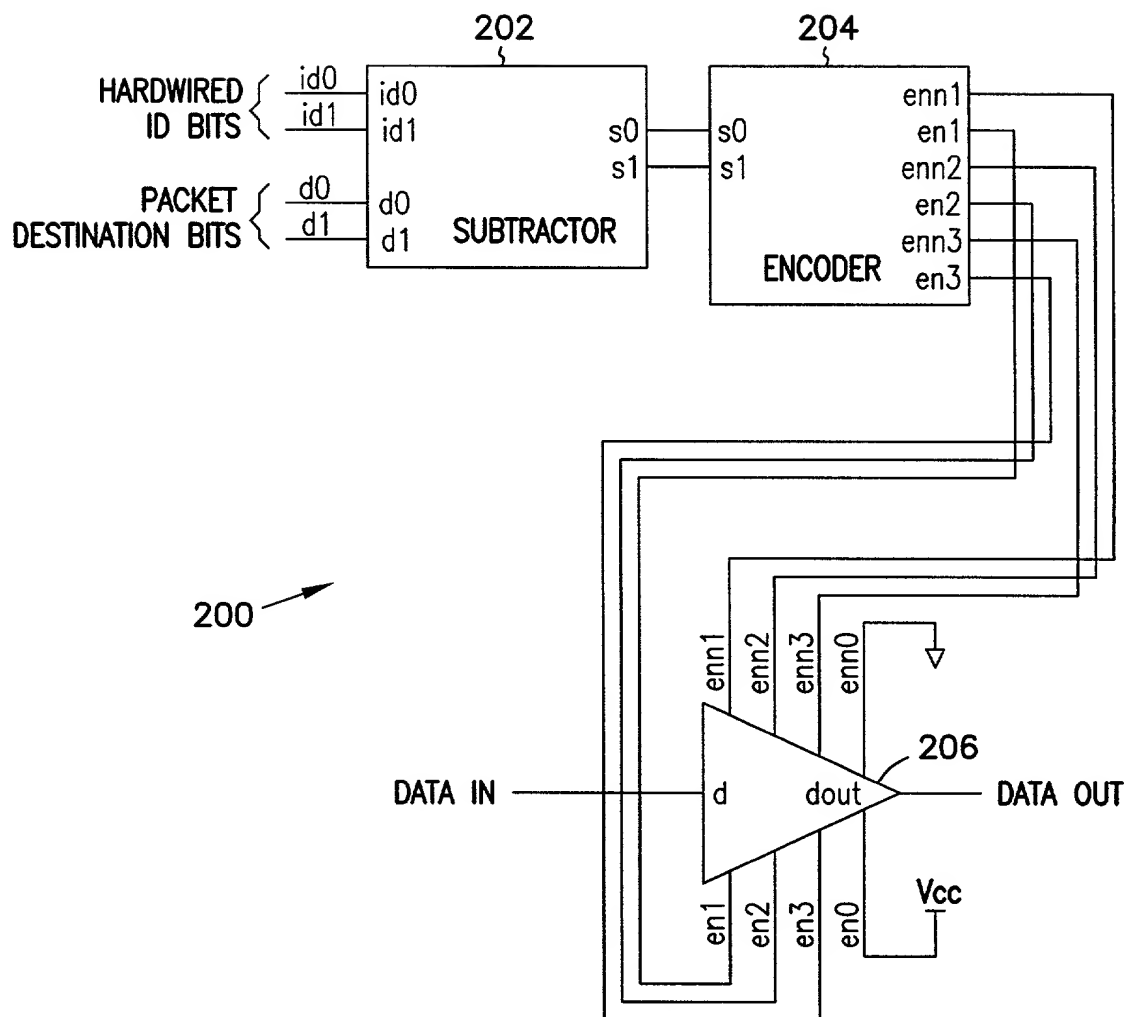


FIG. 2

FIG. 3

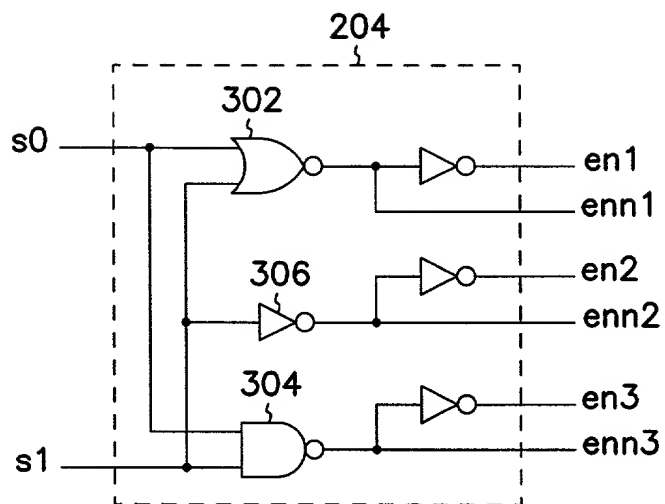
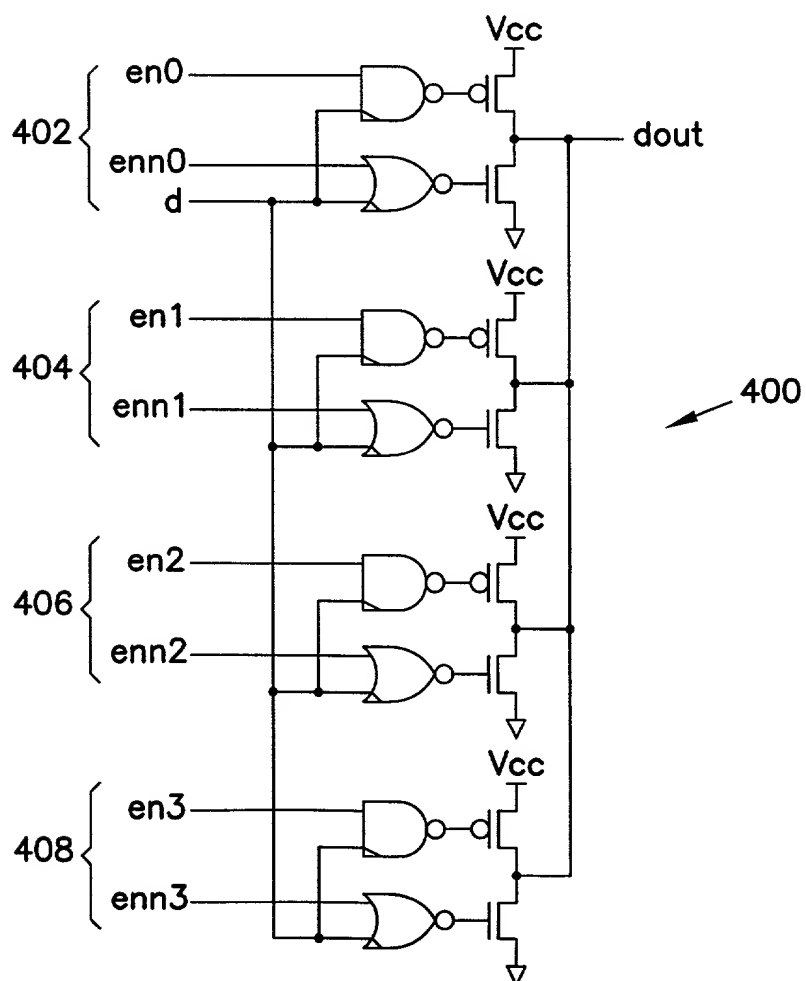


FIG. 4



	1990-1991	1991-1992	1992-1993	1993-1994	1994-1995	1995-1996	1996-1997	1997-1998	1998-1999	1999-2000	2000-2001	2001-2002	2002-2003	2003-2004	2004-2005	2005-2006	2006-2007	2007-2008	2008-2009	2009-2010	2010-2011	2011-2012	2012-2013	2013-2014	2014-2015	2015-2016	2016-2017	2017-2018	2018-2019	2019-2020	2020-2021	2021-2022	2022-2023	2023-2024	2024-2025	2025-2026	2026-2027	2027-2028	2028-2029	2029-2030	2030-2031	2031-2032	2032-2033	2033-2034	2034-2035	2035-2036	2036-2037	2037-2038	2038-2039	2039-2040	2040-2041	2041-2042	2042-2043	2043-2044	2044-2045	2045-2046	2046-2047	2047-2048	2048-2049	2049-2050	2050-2051	2051-2052	2052-2053	2053-2054	2054-2055	2055-2056	2056-2057	2057-2058	2058-2059	2059-2060	2060-2061	2061-2062	2062-2063	2063-2064	2064-2065	2065-2066	2066-2067	2067-2068	2068-2069	2069-2070	2070-2071	2071-2072	2072-2073	2073-2074	2074-2075	2075-2076	2076-2077	2077-2078	2078-2079	2079-2080	2080-2081	2081-2082	2082-2083	2083-2084	2084-2085	2085-2086	2086-2087	2087-2088	2088-2089	2089-2090	2090-2091	2091-2092	2092-2093	2093-2094	2094-2095	2095-2096	2096-2097	2097-2098	2098-2099	2099-2100	2100-2101	2101-2102	2102-2103	2103-2104	2104-2105	2105-2106	2106-2107	2107-2108	2108-2109	2109-2110	2110-2111	2111-2112	2112-2113	2113-2114	2114-2115	2115-2116	2116-2117	2117-2118	2118-2119	2119-2120	2120-2121	2121-2122	2122-2123	2123-2124	2124-2125	2125-2126	2126-2127	2127-2128	2128-2129	2129-2130	2130-2131	2131-2132	2132-2133	2133-2134	2134-2135	2135-2136	2136-2137	2137-2138	2138-2139	2139-2140	2140-2141	2141-2142	2142-2143	2143-2144	2144-2145	2145-2146	2146-2147	2147-2148	2148-2149	2149-2150	2150-2151	2151-2152	2152-2153	2153-2154	2154-2155	2155-2156	2156-2157	2157-2158	2158-2159	2159-2160	2160-2161	2161-2162	2162-2163	2163-2164	2164-2165	2165-2166	2166-2167	2167-2168	2168-2169	2169-2170	2170-2171	2171-2172	2172-2173	2173-2174	2174-2175	2175-2176	2176-2177	2177-2178	2178-2179	2179-2180	2180-2181	2181-2182	2182-2183	2183-2184	2184-2185	2185-2186	2186-2187	2187-2188	2188-2189	2189-2190	2190-2191	2191-2192	2192-2193	2193-2194	2194-2195	2195-2196	2196-2197	2197-2198	2198-2199	2199-2200	2200-2201	2201-2202	2202-2203	2203-2204	2204-2205	2205-2206	2206-2207	2207-2208	2208-2209	2209-2210	2210-2211	2211-2212	2212-2213	2213-2214	2214-2215	2215-2216	2216-2217	2217-2218	2218-2219	2219-2220	2220-2221	2221-2222	2222-2223	2223-2224	2224-2225	2225-2226	2226-2227	2227-2228	2228-2229	2229-2230	2230-2231	2231-2232	2232-2233	2233-2234	2234-2235	2235-2236	2236-2237	2237-2238	2238-2239	2239-2240	2240-2241	2241-2242	2242-2243	2243-2244	2244-2245	2245-2246	2246-2247	2247-2248	2248-2249	2249-2250	2250-2251	2251-2252	2252-2253	2253-2254	2254-2255	2255-2256	2256-2257	2257-2258	2258-2259	2259-2260	2260-2261	2261-2262	2262-2263	2263-2264	2264-2265	2265-2266	2266-2267	2267-2268	2268-2269	2269-2270	2270-2271	2271-2272	2272-2273	2273-2274	2274-2275	2275-2276	2276-2277	2277-2278	2278-2279	2279-2280	2280-2281	2281
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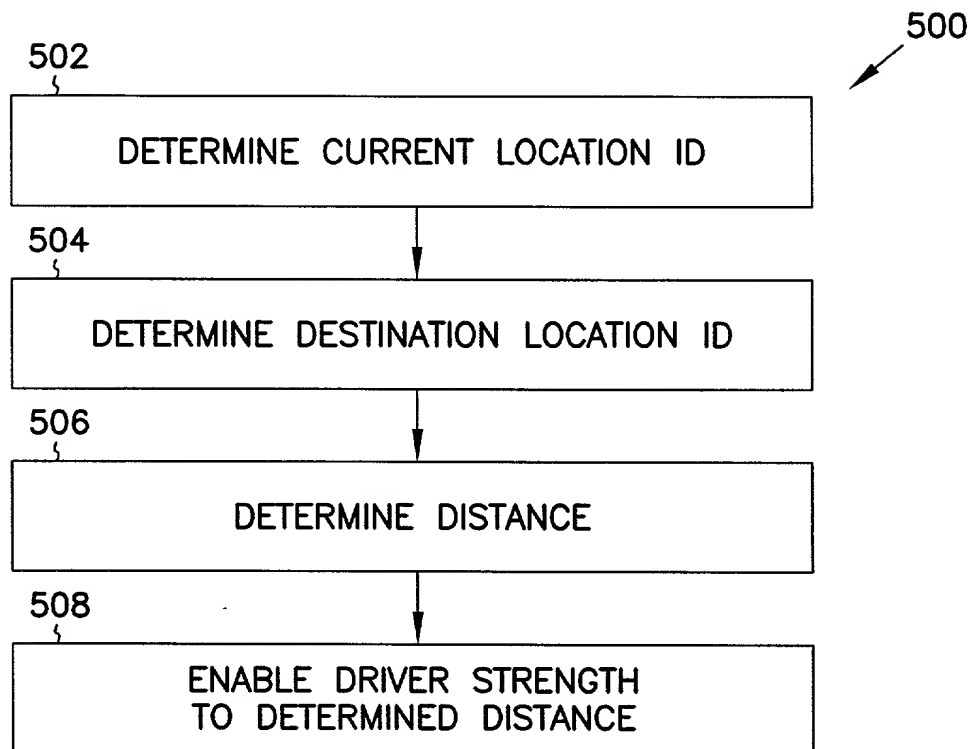


FIG. 5

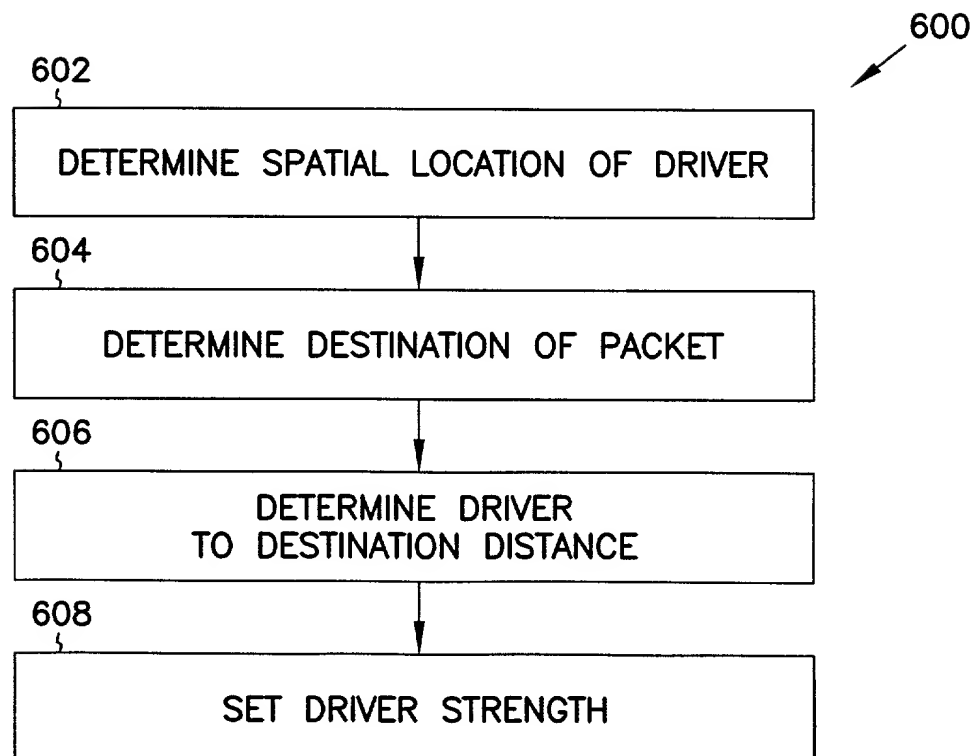


FIG. 6

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

United States Patent Application

COMBINED DECLARATION AND POWER OF ATTORNEY

As a below named inventor I hereby declare that: my residence, post office address and citizenship are as stated below next to my name; that

I verily believe I am the original, first and joint inventor of the subject matter which is claimed and for which a patent is sought on the invention entitled: **METHOD AND APPARATUS FOR DRIVING DATA PACKETS.**

The specification of which is attached hereto.

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to the patentability of this application in accordance with 37 C.F.R. § 1.56 (attached hereto). I also acknowledge my duty to disclose all information known to be material to patentability which became available between a filing date of a prior application and the national or PCT international filing date in the event this is a Continuation-In-Part application in accordance with 37 C.F.R. § 1.63(e).

I hereby claim foreign priority benefits under 35 U.S.C. § 119(a)-(d) or 365(b) of any foreign application(s) for patent or inventor's certificate, or 365(a) of any PCT international application which designated at least one country other than the United States of America, listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on the basis of which priority is claimed:

No such claim for priority is being made at this time.

I hereby claim the benefit under 35 U.S.C. § 119(e) of any United States provisional application(s) listed below:

No such claim for priority is being made at this time.

I hereby claim the benefit under 35 U.S.C. § 120 or 365(c) of any United States and PCT international application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT international application in the manner provided by the first paragraph of 35 U.S.C. § 112, I acknowledge the duty to disclose material information as defined in 37 C.F.R. § 1.56(a) which became available between the filing date of the prior application and the national or PCT international filing date of this application:

No such claim for priority is being made at this time.

I hereby appoint the following attorney(s) and/or patent agent(s) to prosecute this application and to transact all business in the Patent and Trademark Office connected herewith:

Adams, Gregory J.	Reg. No. 44,494	Huebsch, Joseph C.	Reg. No. 42,673	Nelson, Albin J.	Reg. No. 28,650
Anglin, J. Michael	Reg. No. 24,916	Jurkovich, Patti J.	Reg. No. 44,813	Nielsen, Walter W.	Reg. No. 25,539
Bianchi, Timothy E.	Reg. No. 39,610	Kalis, Janal M.	Reg. No. 37,650	Oh, Allen J.	Reg. No. 42,047
Billion, Richard E.	Reg. No. 32,836	Kaufmann, John D.	Reg. No. 24,017	Padys, Danny J.	Reg. No. 35,635
Black, David W.	Reg. No. 42,331	Klima-Silberg, Catherine I.	Reg. No. 40,052	Parker, J. Kevin	Reg. No. 33,024
Brennan, Leoniede M.	Reg. No. 35,832	Kluth, Daniel J.	Reg. No. 32,146	Peacock, Gregg A.	Reg. No. 45,001
Brennan, Thomas F.	Reg. No. 35,075	Lacy, Rodney L.	Reg. No. 41,136	Perdok, Monique M.	Reg. No. 42,989
Brooks, Edward J., III	Reg. No. 40,925	Leffert, Thomas W.	Reg. No. 40,697	Polglaze, Daniel J.	Reg. No. 39,801
Chu, Dinh C.P.	Reg. No. 41,676	Lemaire, Charles A.	Reg. No. 36,198	Prout, William F.	Reg. No. 33,995
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Dahl, John M.	Reg. No. 44,639	Lundberg, Steven W.	Reg. No. 30,568	Schwegman, Micheal L.	Reg. No. 25,816
Drake, Eduardo E.	Reg. No. 40,594	Mack, Lisa K.	Reg. No. 42,825	Slifer, Russell D.	Reg. No. 39,838
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Embretson, Janet E.	Reg. No. 39,665	Malen, Peter L.	Reg. No. 44,894	Steffey, Charles E.	Reg. No. 25,179
Fogg, David N.	Reg. No. 35,138	Mates, Robert E.	Reg. No. 35,271	Terry, Kathleen R.	Reg. No. 31,884
Fordenbacher, Paul J.	Reg. No. 42,546	McCrackin, Ann M.	Reg. No. 42,858	Viksnins, Ann S.	Reg. No. 37,748
Forrest, Bradley A.	Reg. No. 30,837	Nama, Kash	Reg. No. 44,255	Woessner, Warren D.	Reg. No. 30,440
Harris, Robert J.	Reg. No. 37,346				

I hereby authorize them to act and rely on instructions from and communicate directly with the person/assignee/attorney/firm/organization/who/which first sends/sent this case to them and by whom/which I hereby declare that I have consented after full disclosure to be represented unless/until I instruct Schwegman, Lundberg, Woessner & Kluth, P.A. to the contrary.

Please direct all correspondence in this case to **Schwegman, Lundberg, Woessner & Kluth, P.A.** at the address indicated below:

P.O. Box 2938, Minneapolis, MN 55402
Telephone No. (612)373-6900

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

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Post Office Address: **2753 NW Overlook Dr. #2012**
Hillsboro, OR 97124

Residence: **Hillsboro, OR**

Signature: _____

Sriram R. Vangal

Date: _____

Dec 20 '99

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Beaverton, OR 97008

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Signature: _____

Howard A. Wilson

Date: _____

Dec 20, 1999

§ 1.56 Duty to disclose information material to patentability.

(a) A patent by its very nature is affected with a public interest. The public interest is best served, and the most effective patent examination occurs when, at the time an application is being examined, the Office is aware of and evaluates the teachings of all information material to patentability. Each individual associated with the filing and prosecution of a patent application has a duty of candor and good faith in dealing with the Office, which includes a duty to disclose to the Office all information known to that individual to be material to patentability as defined in this section. The duty to disclose information exists with respect to each pending claim until the claim is canceled or withdrawn from consideration, or the application becomes abandoned. Information material to the patentability of a claim that is canceled or withdrawn from consideration need not be submitted if the information is not material to the patentability of any claim remaining under consideration in the application. There is no duty to submit information which is not material to the patentability of any existing claim. The duty to disclose all information known to be material to patentability is deemed to be satisfied if all information known to be material to patentability of any claim issued in a patent was cited by the Office or submitted to the Office in the manner prescribed by §§ 1.97(b)-(d) and 1.98. However, no patent will be granted on an application in connection with which fraud on the Office was practiced or attempted or the duty of disclosure was violated through bad faith or intentional misconduct. The Office encourages applicants to carefully examine:

- (1) prior art cited in search reports of a foreign patent office in a counterpart application, and
- (2) the closest information over which individuals associated with the filing or prosecution of a patent application believe any pending claim patentably defines, to make sure that any material information contained therein is disclosed to the Office.

(b) Under this section, information is material to patentability when it is not cumulative to information already of record or being made of record in the application, and

- (1) It establishes, by itself or in combination with other information, a prima facie case of unpatentability of a claim; or
- (2) It refutes, or is inconsistent with, a position the applicant takes in:
 - (i) Opposing an argument of unpatentability relied on by the Office, or
 - (ii) Asserting an argument of patentability.

A prima facie case of unpatentability is established when the information compels a conclusion that a claim is unpatentable under the preponderance of evidence, burden-of-proof standard, giving each term in the claim its broadest reasonable construction consistent with the specification, and before any consideration is given to evidence which may be submitted in an attempt to establish a contrary conclusion of patentability.

(c) Individuals associated with the filing or prosecution of a patent application within the meaning of this section are:

- (1) Each inventor named in the application;
- (2) Each attorney or agent who prepares or prosecutes the application; and
- (3) Every other person who is substantively involved in the preparation or prosecution of the application and who is associated with the inventor, with the assignee or with anyone to whom there is an obligation to assign the application.

(d) Individuals other than the attorney, agent or inventor may comply with this section by disclosing information to the attorney, agent, or inventor.